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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant: Sanjeev Aggarwal, et al.

Docket No: TI-36294

Serial No: 10/805,121

Conf. No: 8911

Examiner: Douglas M. Menz

Art Unit: 2891

Filed: 03/19/2004


For: BARRIER FOR COPPER INTEGRATED CIRCUITS

ELECTION

Commissioner For Patents
P.O. Box 1450
Alexandria, VA 22313-1450

MAILING CERTIFICATE UNDER 37 C.F.R. § 1.8(a)

I hereby certify that the above correspondence is being deposited with the U.S. Postal Service with sufficient postage as First Class Mail in an envelope addressed to: Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450 on 6-15-05.

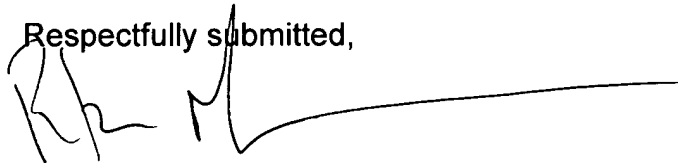

Ann Trent

Dear Sir:

This election is offered in response to the Examiner's restriction requirement mailed May 17, 2005.

Applicants hereby elect to pursue Group I of Claims 1-7, drawn to a copper interconnect structure, without traversing the Examiner's restriction requirement.

Respectfully submitted,



Peter K. McLarty
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